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1 [MASS: modular architecture for sensor systems](#)

Nicholas Edmonds, Doug Stark, Jesse Davis

April 2005 IPSN '05: Proceedings of the 4th international symposium on Information processing in sensor networks

Publisher: IEEE Press

Full text available: [pdf\(221.11 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 25, Citation Count: 2

Recent advances in low power microprocessors and power storage technology have allowed for the design of a broad range of sensor network platforms. Current sensor network platforms fall into one of two main categories, low power, application specific ...

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2 [Workload-based generation of administrator hints for optimizing database storage utilization](#)

Kaushik Dutta, Raju Rangaswami, Sajib Kundu

February 2008 ACM Transactions on Storage (TOS), Volume 3 Issue 4

Publisher: ACM

Full text available: [pdf\(346.97 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 52, Downloads (12 Months): 63, Citation Count: 0

Database storage management at data centers is a manual, time-consuming, and error-prone task. Such management involves regular movement of database objects across storage nodes in an attempt to balance the I/O bandwidth utilization across disk drives. ...

3 [ULoad: choosing the right storage for your XML application](#)

Andrei Arion, Véronique Benzaken, Ioana Manolescu, Ravi Vijay

August 2005 VLDB '05: Proceedings of the 31st international conference on Very large data bases

Publisher: VLDB Endowment

Full text available: [pdf\(104.77 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 30, Citation Count: 0

A key factor for the outstanding success of database management systems is *physical data independence*: queries, and application programs, are able to refer to the data at the logical level, ignoring the details on how the data is physically stored ...

4 [Architectural support for safe software execution on embedded processors](#)

Divya Arora, Anand Raghunathan, Srivaths Ravi, Niraj K. Jha

October 2006 CODES+ISSS '06: Proceedings of the 4th international conference on Hardware/software codesign and system synthesis

Publisher: ACM

Full text available:  pdf(253.62 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 99, Citation Count: 0

The lack of memory safety in many popular programming languages, including C and C++, has been a cause for great concern in the realm of software reliability, verification, and more recently, system security. A major portion of known security attacks ...

Keywords: extensible processors, memory safety, type safety

5 XML-based supply chain simulation modeling

Dean C. Chatfield, Terry P. Harrison, Jack C. Hayya

December 2004 WSC '04: Proceedings of the 36th conference on Winter simulation

Publisher: Winter Simulation Conference

Full text available:  pdf(263.31 KB)

Additional Information: [full citation](#), [abstract](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 68, Citation Count: 0

We describe a different approach to using XML to support the simulation modeling of supply chains. Instead of using XML to specify the simulation constructs, as most previous approaches do, we utilize XML to describe the supply chain itself. The Supply ...

6 SISCO: a supply chain simulation tool utilizing silk™ and XML

Dean C. Chatfield, Terry P. Harrison, Jack C. Hayya

December 2001 WSC '01: Proceedings of the 33rd conference on Winter simulation

Publisher: IEEE Computer Society

Full text available:  pdf(276.14 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 40, Citation Count: 5

We discuss SISCO, the Simulator for Integrated Supply Chain Operations, a Java-based tool that simplifies supply chain simulation model development. SISCO maps supply chain descriptions stored in the XML-based Supply Chain Modeling Language (SCML) format ...

7 Extending document management systems with user-specific active properties

 Paul Dourish, W. Keith Edwards, Anthony LaMarca, John Lampert, Karin Petersen, Michael Salisbury, Douglas B. Terry, James Thornton

April 2000 ACM Transactions on Information Systems (TOIS), Volume 18 Issue 2

Publisher: ACM

Full text available:  pdf(166.43 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 298, Citation Count: 30

Document properties are a compelling infrastructure on which to develop document management applications. A property-based approach avoids many of the problems of traditional hierarchical storage mechanisms, reflects document organizations meaningful ...

Keywords: active properties, component software, document management systems, document services, user experience

8 A design for high-performance flash disks

 Andrew Birrell, Michael Isard, Chuck Thacker, Ted Wobber

April 2007 ACM SIGOPS Operating Systems Review, Volume 41 Issue 2

Publisher: ACM

Full text available:  pdf(241.73 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 34, Downloads (12 Months): 340, Citation Count: 0

Most commodity flash disks exhibit very poor performance when presented with writes that are

not sequentially ordered. We argue that performance can be significantly improved through the addition of sufficient RAM to hold data structures describing a ...

9 [An embedded system case study: the firm ware development environment for a multimedia audio processor](#)

Clifford Liem, Marco Cornero, Miguel Santana, Pierre Paulin, Ahmed Jerraya, Jean-Marc Gentit, Jean Lopez, Xavier Figari, Laurent Bergher

June 1997 [DAC '97: Proceedings of the 34th annual conference on Design automation](#)

Publisher: ACM

Full text available:  [pdf\(52.50 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 29, Citation Count: 1

This paper outlines a case study at SGS-Thomson Microelectronics on the development of a firmware development environment in co-operation with Thomson Consumer Electronics Components. The environment is for an embedded processor used for audiodecompression ...

10 [Register assignment through resource classification for ASIP microcode generation](#)

Clifford Liem, Trevor May, Pierre Paulin

November 1994 [ICCAD '94: Proceedings of the 1994 IEEE/ACM international conference on Computer-aided design](#)

Publisher: IEEE Computer Society Press

Full text available:  [pdf\(653.28 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 20, Citation Count: 12

Application Specific Instruction-Set Processors (ASIPs) offer designers the ability for high-speed data and control processing with the added flexibility needed for late design specifications, accommodation of design errors, and product evolution. However, ...

11 [Introducing USB technology into the undergraduate CS curriculum](#)

Chuck Pheatt

April 2008 [Journal of Computing Sciences in Colleges, Volume 23 Issue 4](#)

Publisher: Consortium for Computing Sciences in Colleges

Full text available:  [pdf\(51.21 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 6, Citation Count: 0

The Universal Serial Bus (USB) has become a standard way to attach mainstream products like cameras, disk drives and keyboards to hosts. Historically, developing firmware and software for such devices has been the realm of electrical and computer engineers ...

12 [Porting a Network Cryptographic Service to the RMC2000: A Case Study in Embedded Software Development](#)

Stephen Jan, Paolo de Dios, Stephen A. Edwards

March 2003 [DATE '03: Proceedings of the conference on Design, Automation and Test in Europe: Designers' Forum - Volume 2, Volume 2](#)

Publisher: IEEE Computer Society

Full text available:  [pdf\(115.07 KB\)](#)

 [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 14, Citation Count: 0

This paper describes our experience porting a transport-layer cryptography service to an embedded microcontroller. We describe some key development issues and techniques involved in porting networked software to a connected, limited resource device such ...

13 Fingerprinting: bounding soft-error detection latency and bandwidth

 Jared C. Smolens, Brian T. Gold, Jangwoo Kim, Babak Falsafi, James C. Hoe, Andreas G. Nowatzky
October 2004 ASPLOS-XI: Proceedings of the 11th international conference on Architectural support for programming languages and operating systems

Publisher: ACM

Full text available:  pdf(229.65 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 107, Citation Count: 8

Recent studies have suggested that the soft-error rate in microprocessor logic will become a reliability concern by 2010. This paper proposes an efficient error detection technique, called *fingerprinting*, that detects differences in execution ...

Keywords: backwards error recovery (BER), dual modular redundancy (DMR), error detection, soft errors

14 Fingerprinting: bounding soft-error detection latency and bandwidth

 Jared C. Smolens, Brian T. Gold, Jangwoo Kim, Babak Falsafi, James C. Hoe, Andreas G. Nowatzky
December 2004 ACM SIGOPS Operating Systems Review, Volume 38 Issue 5

Publisher: ACM

Full text available:  pdf(229.65 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 107, Citation Count: 8

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15 Fingerprinting: bounding soft-error detection latency and bandwidth

 Jared C. Smolens, Brian T. Gold, Jangwoo Kim, Babak Falsafi, James C. Hoe, Andreas G. Nowatzky
November 2004 ACM SIGPLAN Notices, Volume 39 Issue 11

Publisher: ACM

Full text available:  pdf(229.65 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 107, Citation Count: 8

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Keywords: backwards error recovery (BER), dual modular redundancy (DMR), error detection, soft errors

16 Fingerprinting: bounding soft-error detection latency and bandwidth

 Jared C. Smolens, Brian T. Gold, Jangwoo Kim, Babak Falsafi, James C. Hoe, Andreas G. Nowatzky
December 2004 ACM SIGARCH Computer Architecture News, Volume 32 Issue 5

Publisher: ACM

Full text available:  pdf(229.65 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 107, Citation Count: 8

Recent studies have suggested that the soft-error rate in microprocessor logic will become a reliability concern by 2010. This paper proposes an efficient error detection technique, called *fingerprinting*, that detects differences in execution ...

Keywords: backwards error recovery (BER), dual modular redundancy (DMR), error detection, soft errors

17 Enhancing security through hardware-assisted run-time validation of program data properties

Divya Arora, Anand Raghunathan, Srivaths Ravi, Niraj K. Jha
September 2005 CODES+ ISSS '05: Proceedings of the 3rd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis

Publisher: ACM

Full text available:  pdf(194.54 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 52, Citation Count: 0

The growing number of information security breaches in electronic and computing systems calls for new design paradigms that consider security as a primary design objective. This is particularly relevant in the embedded domain, where the security solution ...

Keywords: data tagging, run-time checks, secure architectures

18 Mechanisms for store-wait-free multiprocessors

Thomas F. Wenisch, Anastasia Ailamaki, Babak Falsafi, Andreas Moshovos
June 2007 ACM SIGARCH Computer Architecture News, Volume 35 Issue 2

Publisher: ACM

Full text available:  pdf(535.36 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 34, Downloads (12 Months): 262, Citation Count: 1

Store misses cause significant delays in shared-memory multiprocessors because of limited store buffering and ordering constraints required for proper synchronization. Today, programmers must choose from a spectrum of memory consistency models that reduce ...

Keywords: memory consistency models, store buffer design

19 Mechanisms for store-wait-free multiprocessors

Thomas F. Wenisch, Anastasia Ailamaki, Babak Falsafi, Andreas Moshovos
June 2007 ISCA '07: Proceedings of the 34th annual international symposium on Computer architecture

Publisher: ACM

Full text available:  pdf(535.36 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 34, Downloads (12 Months): 262, Citation Count: 1

Store misses cause significant delays in shared-memory multiprocessors because of limited store buffering and ordering constraints required for proper synchronization. Today, programmers must choose from a spectrum of memory consistency models that reduce ...

Keywords: memory consistency models, store buffer design

20 Page composition using PPML as a link-editing script

Steven R. Bagley, David F. Brailsford
October 2004 DocEng '04: Proceedings of the 2004 ACM symposium on Document engineering

Publisher: ACM

Full text available:  pdf(197.33 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 17, Citation Count: 3

The advantages of a COG (Component Object Graphic) approach to the composition of PDF pages have been set out in a previous paper [1]. However if pages are to be composed in this way then the individual graphic objects must have known bounding boxes ...

Keywords: PDF, PPML, form Xobjects, graphic objects, link editing

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